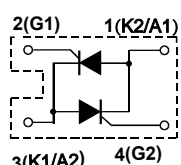
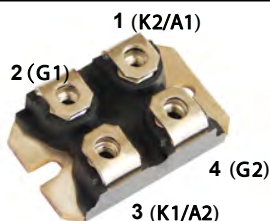
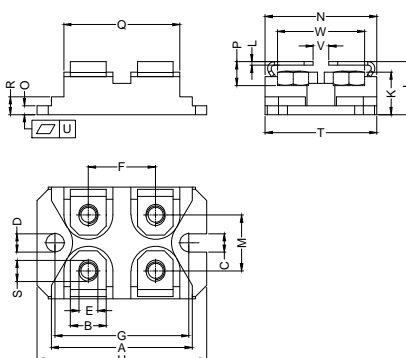


# SSAC62G16S

## Anti-Parallel Thyristor-Thyristor Modules(Solid State AC Switches)



Dimensions SOT-227



Dim.	Millimeter		Dim.	Millimeter	
	Min.	Max.		Min.	Max.
A	31.30	31.62	M	12.20	14.00
B	7.80	8.20	N	25.15	25.62
C	4.09	4.29	O	1.98	2.13
D	4.09	4.29	P	4.15	4.62
E	4.09	4.29	Q	25.90	26.30
F	14.91	15.11	R	3.94	4.42
G	30.12	30.30	S	4.45	4.85
H	38.20	38.56	T	24.59	25.07
J	12.30	12.90	U	0.05	0.10
K	8.92	9.60	V	3.00	4.80
L	0.76	0.84	W	19.81	21.08

Type	$V_{RSM}$	$V_{RRM}$
	$V_{DSM}$	$V_{DRM}$
	V	V
SSAC62G08S	800	800
SSAC62G12S	1200	1200
SSAC62G14S	1400	1400
SSAC62G16S	1600	1600
SSAC62G18S	1800	1800

Symbol	Test Conditions	Maximum Ratings	Unit	
$I_{RMS}$	$T_C = 110^\circ C$ , 50 - 400 Hz, module	54	A	
$I_{TRMS}$	$T_{VJ} = T_{VJM}$	39		
$I_{TAVM}$	$T_C = 110^\circ C$ ; (180° sine)	25		
$I_{TSM}$	$T_{VJ} = 45^\circ C$	$t = 10ms$ (50Hz), sine	400	A
	$V_R = 0$	$t = 8.3ms$ (60Hz), sine	300	
	$T_{VJ} = T_{VJM}$	$t = 10ms$ (50Hz), sine	350	
	$V_R = 0$	$t = 8.3ms$ (60Hz), sine	370	
$I^2t$	$T_{VJ} = 45^\circ C$	$t = 10ms$ (50Hz), sine	800	$A^2s$
	$V_R = 0$	$t = 8.3ms$ (60Hz), sine	780	
	$T_{VJ} = T_{VJM}$	$t = 10ms$ (50Hz), sine	610	
	$V_R = 0$	$t = 8.3ms$ (60Hz), sine	570	
$(di/dt)_{cr}$	$T_{VJ} = T_{VJM}$ $f = 50Hz$ , $t_p = 200\mu s$ $V_D = 2/3V_{DRM}$ $I_G = 0.3A$ $di/dt = 0.3A/\mu s$	repetitive, $I_T = 150A$	100	A/ $\mu s$
		non repetitive, $I_T = I_{TAVM}$	500	
$(dv/dt)_{cr}$	$T_{VJ} = T_{VJM}$ ; $R_{GK} = \infty$ ; method 1 (linear voltage rise)	$V_{DR} = 2/3V_{DRM}$	1000	V/ $\mu s$
$P_{GM}$	$T_{VJ} = T_{VJM}$ $I_T = I_{TAVM}$	$t_p = 30\mu s$	10	W
		$t_p = 300\mu s$	5	
$P_{GAVM}$			0.5	
$V_{RGM}$			10	V
$T_{VJ}$			-40...+150	$^\circ C$
$T_{VJM}$			150	
$T_{stg}$			-40...+150	
$V_{ISOL}$	50/60Hz, RMS $I_{ISOL} \leq 1mA$		2500	V~
$M_d$	Mounting torque (M4)		1.1-1.5/9-13	Nm/lb.in.
	Terminal connection torque (M4)		1.1-1.5/9-13	
Weight	typical		30	g

**Sirectifier®**

# SSAC62G16S

## Anti-Parallel Thyristor-Thyristor Modules(Solid State AC Switches)

Symbol	Test Conditions	Characteristic Values	Unit
$I_R, I_D$	$T_{VJ}=T_{VJM}; V_R=V_{RRM}; V_D=V_{DRM}$	$\leq 12$	mA
$V_T$	$I_T=45A; T_{VJ}=25^{\circ}C$	$\leq 1.57$	V
$V_{TO}$	For power-loss calculations only	$\leq 0.85$	V
$r_T$		$\leq 12$	m $\Omega$
$V_{GT}$	$V_D=6V; T_{VJ}=25^{\circ}C$ $T_{VJ}=-40^{\circ}C$	$\leq 1.5$ $\leq 1.6$	V
$I_{GT}$	$V_D=6V; T_{VJ}=25^{\circ}C$ $T_{VJ}=-40^{\circ}C$	$\leq 100$ $\leq 150$	mA
$V_{GD}$	$T_{VJ}=T_{VJM}; V_D=2/3V_{DRM}$	$\leq 0.2$	V
$I_{GD}$		$\leq 5$	mA
$I_L$	$T_{VJ}=25^{\circ}C; t_p=10\mu s$ $I_G=0.3A; di_G/dt=0.3A/\mu s$	$\leq 250$	
$I_H$	$T_{VJ}=25^{\circ}C; V_D=6V; R_{GK}=\infty$	$\leq 100$	
$t_{gd}$	$T_{VJ}=25^{\circ}C; V_D=1/2V_{DRM}$ $I_G=0.3A; di_G/dt=0.3A/\mu s$	$\leq 2$	us
$t_q$	$T_{VJ}=T_{VJM}; I_T=20A; t_p=200\mu s; di/dt=-10A/\mu s$ $V_R=100V; dv/dt=15V/\mu s; V_D=2/3V_{DRM}$ typ.	$\leq 150$	
$R_{thJC}$	per thyristor; DC current per module	$\leq 0.91$ $\leq 0.455$	K/W
$R_{thCH}$	per thyristor; DC current per module typ. typ.	$\leq 0.1$ $\leq 0.05$	
$d_s$	Creeping distance on surface	$\leq 8$	mm
$d_A$	Creepage distance in air	$\leq 4$	
$a$	Max. allowable acceleration	$\leq 50$	m/s <sup>2</sup>

### FEATURES

- \* Thyristor controller for AC for mains frequency
- \* International standard package SOT-227
- \* Isolation voltage 2500V~
- \* Glass passivated chips
- \* UL File NO. E310749
- \* RoHS compliant

### APPLICATIONS

- \* Switching and control of single and three phase AC Softstart
- \* AC motor controller
- \* Solid state Switches
- \* Light and temperature control

### ADVANTAGES

- \* Easy to mount with two screws
- \* Space and weight savings
- \* Improved temperature and power cycling
- \* High power density



**Sirectifier®**

# SSAC62G16S

## Anti-Parallel Thyristor-Thyristor Modules(Solid State AC Switches)

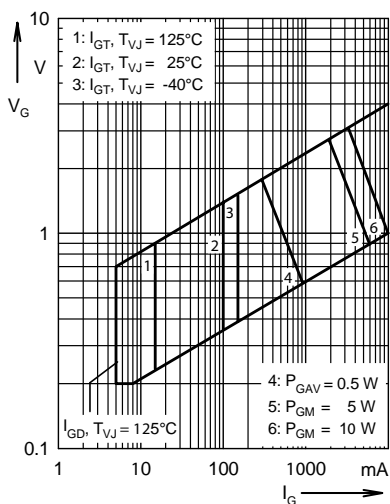


Fig. 1 Gate trigger characteristics

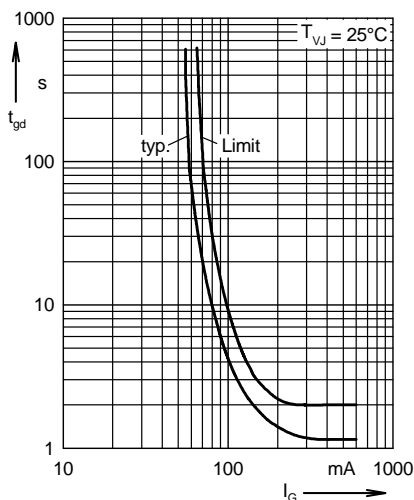


Fig. 2 Gate trigger delay time

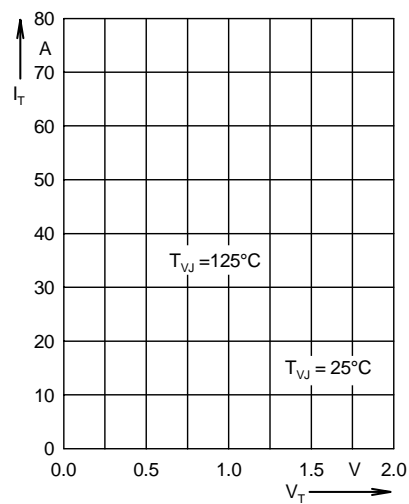


Fig. 3 Forward current versus voltage drop per leg

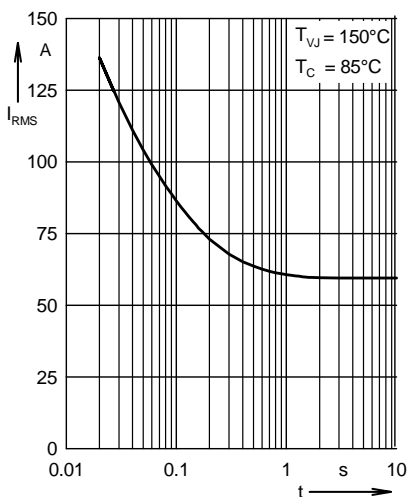


Fig. 4 Rated RMS current versus time (360° conduction)

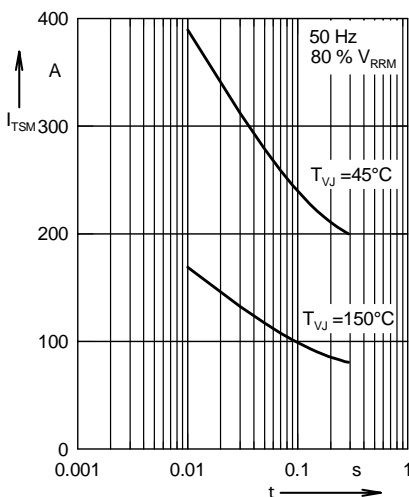


Fig. 5 Surge overload current

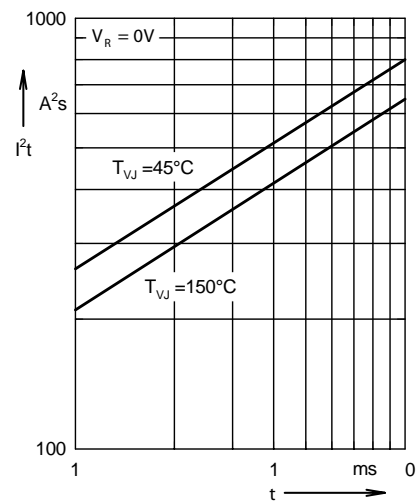


Fig. 7  $I^2t$  versus time (per thyristor)

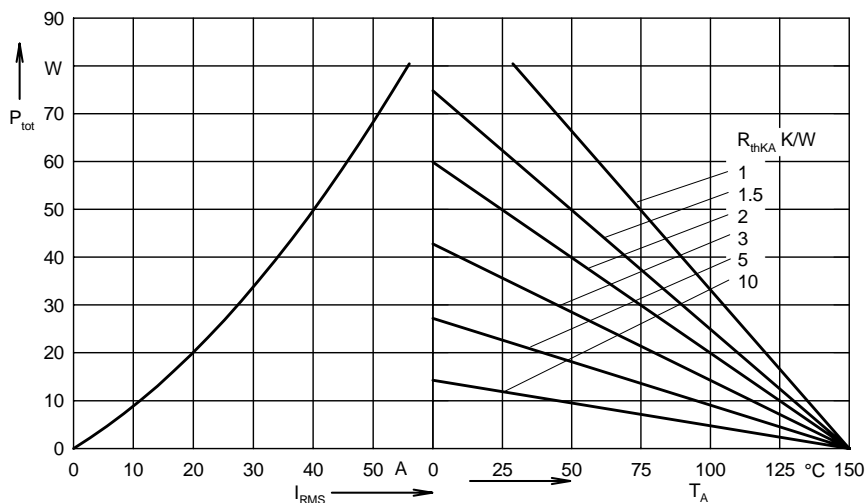


Fig. 6 Load current capability for single AC controller; 1 x SSAC62



# SSAC62G16S

## Anti-Parallel Thyristor-Thyristor Modules(Solid State AC Switches)

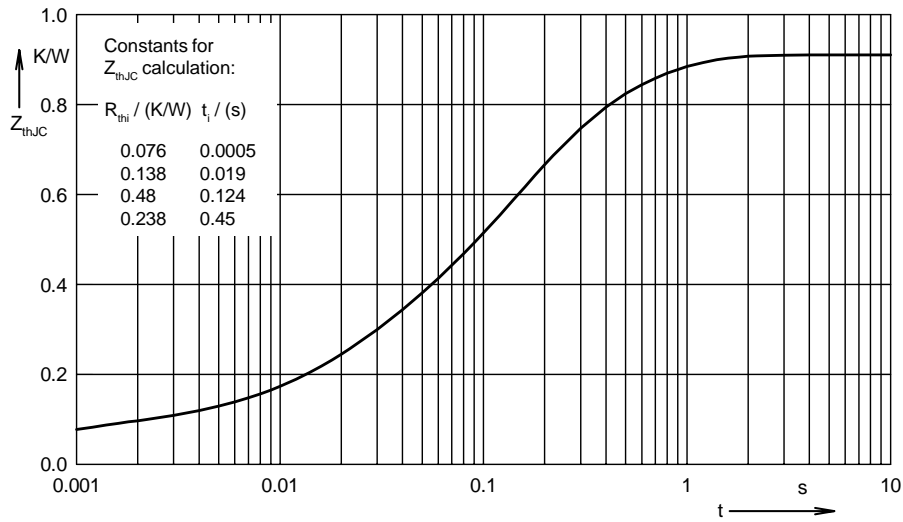


Fig. 8 Transient thermal impedance junction to case (per thyristor)

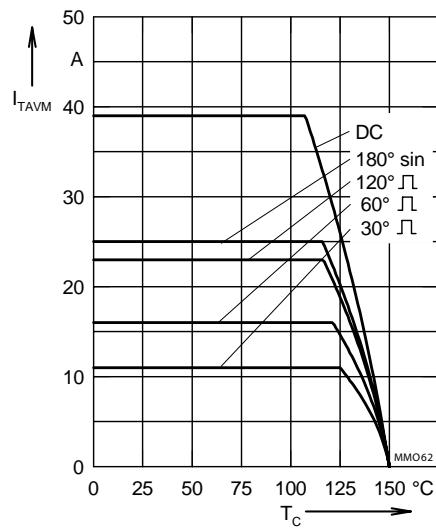


Fig. 9 Maximum forward current at case temperature